

# RKS100KG

Silicon Epitaxial Planar Diode for High Speed Switching

REJ03G1698-0100 Rev.1.00 Jul 03, 2008

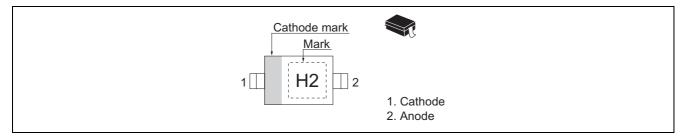
# Features

- Low capacitance. (C = 2.0 pF max)
- Short reverse recovery time. ( $t_{rr} = 3.0 \text{ ns max}$ )
- Ultra small Resin Package (URP) is suitable for high density surface mounting and high speed assembly.

# **Ordering Information**

Part No.	Laser Mark	Package Name	Package Code	
RKS100KG	H2	URP	PTSP0002ZA-A	

# **Pin Arrangement**



# **Absolute Maximum Ratings**

			$(Ta = 25^{\circ}C)$
Item	Symbol	Value	Unit
Peak reverse voltage	V <sub>RM</sub>	85	V
Reverse voltage	V <sub>R</sub>	80	V
Forward current	I <sub>F</sub> * <sup>1</sup>	200	mA
Non-Repetitive peak forward surge current	I <sub>FSM</sub> * <sup>2</sup>	4	А
Junction temperature	Тј	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. Forward current with mounting on the board of Figure 1. 2. Within 1 µs forward surge current.

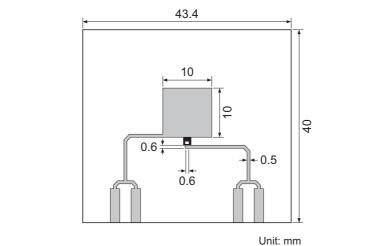
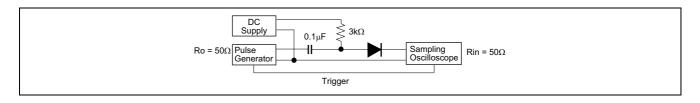


Figure 1 Board

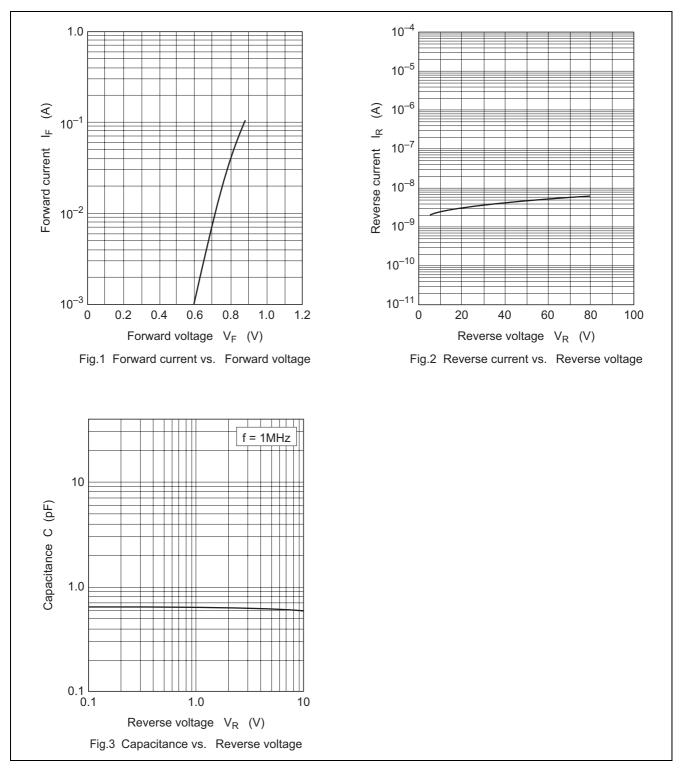
# **Electrical Characteristics**

						$(Ta = 25^{\circ}C)$
ltem	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	V <sub>F1</sub>	—		0.8	V	I <sub>F</sub> = 10 mA
	V <sub>F2</sub>	—		1.2		I <sub>F</sub> = 100 mA
Reverse current	I <sub>R</sub>	—		0.1	μA	V <sub>R</sub> = 80 V
Capacitance	С	—		2.0	pF	$V_{R} = 0 V, f = 1 MHz$
Reverse recovery time*1	t <sub>rr</sub>			3.0	ns	$I_F=10~mA,~V_R=6~V,~R_L=50~\Omega$

Note: 1. Reverse recovery time test circuit

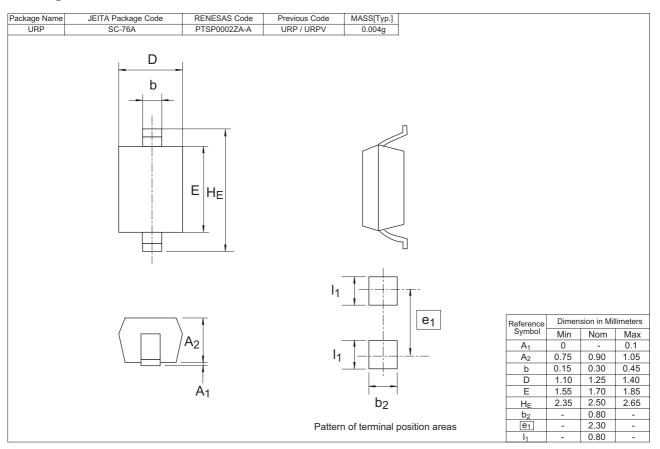


# **Main Characteristics**



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# **Package Dimensions**



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